



U.S. PTO

Attorney Docket No. 030681-624

Patent

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

UTILITY PATENT  
APPLICATION TRANSMITTAL LETTER

Mail Stop PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Customer Number 21839

17497 U.S. PTO  
10/758136

Sir:

Enclosed for filing is the utility patent application entitled:

METHOD OF MANUFACTURING SILICON OPTOELECTRONIC DEVICE, SILICON OPTOELECTRONIC DEVICE MANUFACTURED BY THE METHOD, AND IMAGE INPUT AND/OR OUTPUT APPARATUS USING THE SILICON OPTOELECTRONIC DEVICE

by the following named inventor(s):

Eun-kyung LEE, Byoung-lyong CHOI, Pil-soo AHN, Jun-young KIM and Young-gu JIN

- Applicant(s) suggests Figure 12 for inclusion on the front page of the patent application publication and patent.
- Applicant(s) requests that the published application include the following assignment information:  
Samsung Electronics Co., Ltd., Gyeonggi-do, Republic of Korea

- Small entity status is claimed.

Also enclosed are:

DRAWINGS: 16 sheets of formal drawings \_\_\_\_\_ sheets of informal drawingsDECLARATION:  will follow  executed, is enclosed  unexecuted, is enclosedASSIGNMENT:  is enclosed  will follow

**CLAIM FOR  
PRIORITY  
UNDER 35 U.S.  
C. § 119 and/or  
365:**

certified copy(ies) enclosed       certified copy(ies) will follow

certified copy(ies) will follow

**OTHER PAPERS:**  a General Authorization for Petitions for Extensions of Time and Payment of Fees  
 an Information Disclosure Statement  
 an Application Data Sheet (ADS)

The filing fee has been calculated as follows  and in accordance with the enclosed preliminary amendment:

| CLAIMS   |                  |            |              |                    |                    |
|--|------------------|------------|--------------|--------------------|--------------------|
|  | No.<br>of Claims |            | Extra Claims | Rate               | Fee                |
| Basic Application Fee (1001)   |                  |            |              |                    | \$ 770.00          |
| Total Claims   | 35               | MINUS 20 = | 15           | x \$18.00 (1202) = | \$ 270.00          |
| Independent Claims   | 2                | MINUS 3 =  | 0            | x \$86.00 (1201) = | \$ 0.00            |
| If multiple dependent claims are presented, add \$290.00 (1203)                              |                  |            |              |                    |                    |
| Total Application Fee  |                  |            |              |                    | \$ 1,040.00        |
| <input type="checkbox"/> Small Entity Status claimed - subtract 50% of Total Application Fee |                  |            |              |                    | \$ 0.00            |
| Add Assignment Recording Fee of \$40.00 (8021) if Assignment document is enclosed.           |                  |            |              |                    |                    |
| <b>TOTAL APPLICATION FEE DUE</b>   |                  |            |              |                    | <b>\$ 1,040.00</b> |

- This application is being filed without a filing fee. Issuance of a Notice to File Missing Parts of Application is respectfully requested.

Charge \_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.

A check in the amount of \$ 1,040.00 is enclosed for the fee due.

- The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Please address all correspondence concerning this application to:

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Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By



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Filed: January 16, 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

Eun-kyung LEE et al

Group Art Unit: Unassigned

Application No.: Unassigned

Examiner: Unassigned

Filing Date: January 16, 2004

Confirmation No.: Unassigned

Title: METHOD OF MANUFACTURING SILICON OPTOELECTRONIC DEVICE, SILICON  
OPTOELECTRONIC DEVICE MANUFACTURED BY THE METHOD, AND IMAGE INPUT AND/OR  
OUTPUT APPARATUS USING THE SILICON OPTOELECTRONIC DEVICE

**GENERAL AUTHORIZATION FOR PETITIONS  
FOR EXTENSIONS OF TIME AND PAYMENT OF FEES**

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.136(a)(3), the U.S. Patent and Trademark Office is hereby provided with a general authorization to treat any concurrent or future reply requiring a petition for an extension of time for its timely submission as containing a request therefor for the appropriate length of time.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. § 1.17 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By

  
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